

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Anastasios S. Maurudis, John O. Della Morte and James T. Della Morte  
Application No.: 10/675,248 Group Art Unit: 2123  
Filed: September 30, 2003 Examiner: Not Assigned  
Confirmation No.: 3497  
Title: METHOD AND APPARATUS FOR ACCURATELY MODELING  
DIGITAL SIGNAL PROCESSORS

CERTIFICATE OF MAILING OR TRANSMISSION	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, or is being facsimile transmitted to the United States Patent and Trademark Office on:	
<u>March 9, 2004</u>	<u>Elaine Leahy</u>
Date	Signature
<u>Elaine Leahy</u>	
Typed or printed name of person signing certificate	

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This Information Disclosure Statement is submitted:

- ☐ under 37 CFR 1.129(a), or  
(First/Second submission after Final Rejection)
- ☒ under 37 CFR 1.97(b), or  
(Within any one of the following time periods: three months of filing national application (other than a CPA) or date of entry of the national stage in an international application; or before the mailing date of a first office action on the merits in a non-provisional application, including a CPA, or a Request for Continued Examination).
- ☐ under 37 CFR 1.97(c) together with either:
- ☐ a Statement under 37 CFR 1.97(e), as checked below, or
- ☐ a \$180.00 fee under 37 CFR 1.17(p), or  
(After the 37 CFR 1.97(b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97(d) together with:
- ☐ a Statement under 37 CFR 1.97(e), as checked below, and
- ☐ a \$180.00 fee under 37 CFR 1.17(p), or  
(Filed after final action or notice of allowance, whichever occurs first, but on or before payment of the issue fee)
- ☐ under 37 CFR 1.97(i):  
Applicant requests that the IDS and cited reference(s) be placed in the application filewrapper.  
(Filed after payment of issue fee)

Statement Under 37 CFR 1.97(e)

- ☐ Each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement; or
- ☐ No item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

Statement Under 37 CFR 1.704(d) (Patent Term Adjustment)

Applies to original applications (other than design) filed on or after May 29, 2000

- ☐ Each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of the Information Disclosure Statement.
- ☒ Enclosed herewith is form PTO-1449:
- ☒ Copies of the cited references are enclosed.
- ☒ Since this application was filed after June 30, 2003, copies of issued U.S. patents and published U.S. applications are not required and are not being provided.
- ☐ Copies of the cited references are enclosed except those entered in prior application, U.S. Application No. [ ], to which priority under 35 U.S.C. 120 is claimed. [The earlier application contains copies of the cited references.]
- ☐ The listed references were cited in the enclosed International Search Report in a counterpart foreign application.
- ☐ The "concise explanation" requirement (non-English references) for reference(s) [ ] under 37 CFR 1.98(a)(3) is satisfied by:
- ☐ the explanation provided on the attached sheet.
- ☐ the explanation provided in the Specification.
- ☐ submission of the enclosed International Search Report.
- ☐ submission of the enclosed English-language version of a foreign Search Report and/or foreign Office Action.
- ☐ the enclosed English language abstract.

[X ] Applicant requests that the following non-published pending applications be considered:

Examiner's  
Initials

\_\_\_\_ U.S. Patent Application No. 09/713,732, by Anastasios S. Maurudis, John O. Della Morte, Jr., and James T. Della Morte, filed November 15, 2000, Docket No.: 2376.1001-002

\_\_\_\_ U.S. Patent Application No. [ ], by [inventor(s)], filed [ ], Docket No.: [ ]

\_\_\_\_ U.S. Patent Application No. [ ], by [inventor(s)], filed [ ], Docket No.: [ ]

\_\_\_\_\_  
Examiner

\_\_\_\_\_  
Date

[ X ] A copy of each above-cited application, including the current claims, is enclosed.

[ ] A copy of each above-cited application, including the current claims, is enclosed, except those entered in prior application, U.S. Application No. [ ], to which priority under 35 U.S.C. 120 is claimed.

The Examiner is requested to return a copy of the above list of pending applications indicating which references were considered with the next office communication.

It is requested that the information disclosed herein be made of record in this application.

Method of payment:

[ ] A check for the fee noted above is enclosed, or the fee has been included in the check with the accompanying Reply. A copy of this Statement is enclosed.

[ ] Please charge Deposit Account 08-0380 in the amount of \$[ ]. A copy of this Statement is enclosed.

[X] Please charge any deficiency in fees and credit any overpayment to Deposit Account 08-0380.

Respectfully submitted,

HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

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Dated: 3/9/04

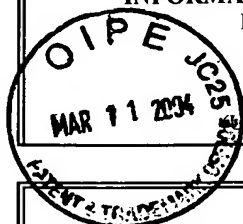
PTO-1449 REPRODUCED

ATTORNEY DOCKET NO.  
2376.1001-003APPLICATION NO.  
10/675,248INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

March 2, 2004

FIRST NAMED INVENTOR  
Anastasios S. MaurudisFILING DATE  
September 30, 2003EXAMINER  
Not assignedCONFIRMATION NO.  
3497GROUP  
2123

(Use several sheets if necessary)



## U.S. PATENT DOCUMENTS

EXAM- INER INI- TIAL	REF. NO.	DOCUMENT NUMBER Number-Kind Code (if known)	ISSUE DATE / PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE OR APPLICANT OF CITED DOCUMENT
	AA	4,893,267	01/09/1990	Alsup <i>et al.</i>
	AB	5,295,222	03/15/1994	Wadhwa <i>et al.</i>
	AC	5,583,983	12/10/1996	Schmitter
	AD	4,135,242	01/16/1979	Ward <i>et al.</i>
	AE	5,560,013	09/24/1996	Scalzi <i>et al.</i>
	AF	5,613,098	03/18/1997	Landau <i>et al.</i>
	AG	5,768,593	06/16/1998	Walters <i>et al.</i>
	AH	6,173,247 B1	01/09/2001	Maurudis <i>et al.</i>
	AI	6,011,872	01/04/2000	Qian <i>et al.</i>
	AJ	5,732,005	03/24/1998	Kahle <i>et al.</i>
	AK			
	AA2			
	AB2			
	AC2			
	AD2			
	AE2			
	AF2			
	AG2			

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER Country Code-Number-Kind Code (if known)	DATE MM-DD-YYYY	NAME OF PATENTEE OR APPLICANT OF CITED DOCUMENT	TRANSLATION YES NO	
	AL	EP 0 718 757 A2	06/26/1996	Motorola	
	AM				
	AN				

EXAMINER

DATE CONSIDERED

PTO-1449 REPRODUCED  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>March 2, 2004</b>  (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2376.1001-003		APPLICATION NO. 10/675,248	
	FIRST NAMED INVENTOR Anastasios S. Maurudis		FILING DATE September 30, 2003	
	EXAMINER Not assigned		CONFIRMATION NO. 3497	GROUP 2123

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
AR	Maurudis, A.S., "FACT™: A C++ Environment for Accurately Modeling Fixed-Point Digital Signal Processors," presented at conference on Intelligent Methods for Signal Processing and Communications, Universidad de Vigo, Baiona (Vigo), Spain, June 24-26, 1996.	
AS	Maurudis, A.S., "FACT™: A C++ Environment for Accurately Modeling Fixed-Point Digital Signal Processors," <i>The Proceedings of the 7th International Conference on Signal Processing Applications &amp; Technology</i> , Vol. 1, p.846-851, Boston, Mass., U.S.A., October 7-10, 1996.	
AT	Maurudis, A.S., "An Efficient Vector-Space Approach for Accurately Modeling Fixed-Point Digital Signal Processors," <i>1996 IEEE TENCON - Digital Signal Processing Applications</i> , pp. 659-664 (November 27-29, 1996).	
AU	Ombres, D., "C and C++ Extensions Simplify Fixed-Point DSP Programming," <i>EDN</i> , pp. 135-138, October 10, 1996.	
AV	"Digital Signal Processing Solutions Support," <a href="http://www.ti.com/sc/docs/dsp/develop/3rdparty/consult/458tarta.htm">http://www.ti.com/sc/docs/dsp/develop/3rdparty/consult/458tarta.htm</a> , (downloaded 6/3/97).	
AW	Harton, M. and K. Kapsucinski, "BEC++" A software tool for increased flexibility in algorithm development," <i>IEEE 0-7803-5651-9/99</i> , pp. 67-69.	
AX	Edwards, C., "Library to model DSP Algorithms," <i>Electronics Times</i> No. 908, p.14, June 1998.	
AY	Robe, E.D. and D. Irwin, "SIMULINK, Modules that Emulate Digital Controllers Realized with Fixed-Point or Floating-Point Arithmetic," <i>IEEE paper; 0-8186-5320-5/94</i> , 1994, pp. 337-341.	
AZ	Kraeling, M.B., "Fixed-Point Math in Time-Critical Applications," <i>IEEE, WESCON/96</i> , October 1996, pages 587-593.	
AR2	Kambi, S.J., <i>et al.</i> , "Error Analysis of Filters Implemented with Floating Point Arithmetic," <i>Proceedings of the 26<sup>th</sup> Southeastern Symposium on System Theory</i> , IEEE, March 1994, pages 47-51.	
AS2	Lee, <i>et al.</i> , "Target Bit Matching for MPEG-2 Video Rate Control," <i>IEEE Region 10 International Conference on Global Connections, Energy, Computer, Communication and Control</i> , December 1998, pages 66-69.	

EXAMINER	DATE CONSIDERED
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PTO-1449 REPRODUCED  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>March 2, 2004</b>  (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2376.1001-003		APPLICATION NO. 10/675,248	
	FIRST NAMED INVENTOR Anastasios S. Maurudis		FILING DATE September 30, 2003	
	EXAMINER Not assigned	CONFIRMATION NO. 3497	GROUP 2123	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
	AT2	Fortune, S. And Van Wyk, C.J., "Efficient Exact Arithmetic for Computational Geometry," 0-89791-583-6/93/0005/0163, 1993, pp 163-172.

EXAMINER	DATE CONSIDERED
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